

# **METHOD OF WRITING DATA TO A LARGE BLOCK OF A FLASH MEMORY CELL**

## **BACKGROUND OF THE INVENTION**

### **1. The field of the invention**

5           **[0001]** The present invention relates to a method of writing data to a large block of a flash memory cell, and more particularly relates to a method of writing data to a large block of a flash memory cell in which the data can be written continuously to a page even when the change of data is unable fully fill up the whole page.

### **2. Description of the related art**

10           **[0002]** As the demand for NAND flash memory cell in the market is increasing, the productivity and the process technology are also upgraded accordingly, such as the development of new type of NAND flash memory cell, for example, a large block flash memory cell, a MLC flash memory cell and the like. And as technology is improved, the unit price of the flash memory cell is getting lower and lower. However, during the  
15 operation, the data storage or data retrieval of each page of the flash memory cell can be implemented only once at a time, and writing the data to the flash memory cell is limited by a sequence of operation, thus the processing speed of writing data to the flash memory cell is obviously reduced correspondingly.

**[0003]** The method of storing and retrieving data from the NAND flash memory  
20 cell can be generally described as follows.

**[0004]** Referring to FIG. 1, the controller picks one block from the preset back up blocks in the flash memory cell and use it as a buffer 11, and writes the unchanged data

into the block 12 of the flash memory cell from the first page of the block 12 in an orderly manner, page by page to the buffer 11.

[0005] Referring to FIG. 2, if the changed data in the block 12 is enough to fill up a whole page, then the data will be written into the buffer 11 page by page according to the page number.

[0006] Referring to FIG. 3, if the changed data in the block 12 is insufficient to fill up a whole page, the processor uses the data 13 of the original block 12 to fill up the remaining free space and write to the buffer 11.

[0007] Furthermore, referring to FIG. 4, writing the unchanged data below the changed data in the block 12 in an orderly manner, page by page to the buffer 11 until writing of data in of the all pages is completed, then the original block 12 is erased to serve as a back up block, and the original block 12 replaced by the buffer 11.

[0008] However, the above conventional data writing method can only move data from the original block to fill up the remaining free space when the space in the page is not completely filled up when data change occurs, and when the data is not written continuously, writing speed will be significantly slow. In an electronic device, such as a computer, which uses this type of flash memory cell, the calculation and processing speed will be significantly reduced.

## **SUMMARY OF THE INVENTION**

[0009] Accordingly, in the view of the foregoing, the present inventor makes a detailed study of related art to evaluate and consider, and uses years of accumulated experience in this field, and through several experiments, to create a new method of writing data to the high speed large block of flash memory cell.

[0010] According to one major aspect of the present invention, when the processor writes data to the flash memory cell, if the changed data can not fill up the whole page, the processor will pick a block from one of the backed up blocks in the flash memory cell using as a special block for storing changed data temporarily, and continue to write the next changed data into the special block until whole space in the page is filled up. When the page is completely filled, then the data stored temporarily in the special block is moved to a buffer of the backed up block and then the special block is erased so that it can be used as a back up block, thus when writing data to flash memory cell, even when the change of data is unable fully fill up the whole page, the changed data still can be written continuously, and therefore the writing speed can be substantially increased.

#### **BRIEF DESCRIPTION OF THE DRAWING**

[0011] For a more complete understanding of the present invention, reference will now be made to the following detailed description of preferred embodiments taken in conjunction with the accompanying drawings, in which:

[0012] FIG. 1 is a block diagram (1) of a conventional data writing method;

[0013] FIG. 2 is a block diagram (2) of the conventional data writing method;

[0014] FIG. 3 is a block diagram (3) of the conventional data writing method;

[0015] FIG. 4 is a block diagram (4) of the conventional data writing method;

[0016] FIG. 5 is a block diagram (1) of a data writing method of the present invention;

[0017] FIG. 6 is a block diagram (2) of the data writing method of the present invention;

[0018] FIG. 7 is a block diagram (3) of the data writing method of the present invention;

[0019] FIG. 8 is a block diagram (4) of the data writing method of the present invention;

5 [0020] FIG. 9 is a block diagram (5) of the data writing method of the present invention;

[0021] FIG. 10 is a block diagram (6) of the data writing method of the present invention;

[0022] FIG. 11 is a block diagram (7) of the data writing method of the present  
10 invention; and

[0023] FIG. 12 is a block diagram (8) of the data writing method of the present invention.

## **DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS**

[0024] Reference will be made in detail to the preferred embodiments of the  
15 invention, examples of which are illustrated in the accompanying drawings. Wherever possible, the same reference numbers are used in the drawings and the description to refer to the same or like parts.

[0025] The present invention relates to a method of writing data to the high speed  
large block of flash memory cell. When the processor writes data to the flash memory  
20 cell, if data changed is unable completely fill the page, the processor will pick a block from the back up blocks of the flash memory cell as the special block for writing the changed data into the special block for temporarily storage until the page is completely filled, and then the temporarily stored data from the special block is moved to one of the

buffer of the back up block. Next, the special block is erased to serve as a back up block. Therefore, even when the changed data is unable to completely fill the page, the changed data can still be written continuously to substantially increase the writing speed.

[0026] In a preferred embodiment of the present invention, referring to FIG. 5, a block is picked to use as a buffer 51 from the preset back up blocks of the flash memory cell, and the unchanged data is written to the block 52 of the flash memory cell from the first page of the block 52 and the unchanged data is moved in an orderly manner according to the page numbers, page by page to the buffer 51.

[0027] Referring to FIG. 6, if the changed data in the block 52 is sufficient to completely fill a page, then the changed data is written page by page according to the page numbers to the buffer 51.

[0028] Furthermore, referring to FIG. 7, whether the changed data in the block 52 is insufficient to completely fill a page is judged, then a block is picked to serve as a special block 53 from the back up blocks and the changed data is written to the special block 53 for temporary storage.

[0029] Referring to FIGs. 8, 9 and 10, whether the next set of data is the continuation of the changed data is judged. If yes, then whether the set of data and the temporary data stored in the special block 53 is sufficient to completely fill the page is judged. If yes, the temporarily stored data is retrieved from the special block 53 and organized together with the set of data for filling up the page then written to the buffer 51, and then the special block 53 is erased for serving as a back up block. If not (i.e. if the set of data and the temporary data stored in the special block 53 is sufficient to completely fill the page), writing of the data to the special block 53 is continued, and the

writing action will be repeated until the page is completely filled. When the page is completely full, then the temporary stored data is retrieved from the special block **53** and the set of data for filling up the page are organized and then the data is written to the buffer **51**, and then the special block **53** is erased to serve as a back up block.

5           **[0030]** Additionally, referring to FIG. **11**, if the next set of data is a continuation of the changed data, the above procedure is repeated, writing the data page by page to the buffer **51** until the page is completely full, then the original block **52** is erased for serving as a back up block and the block **52** is replaced by the buffer **51** to repeat the above procedure.

10           **[0031]** Referring to FIG. **12**, if the next set of data is not the continuation of the changed data, the stored data is read from the special block **53**, and the changed data in the block **52** is organized to fill up the remaining free space **54** and then the data is written to the buffer **51**. Next, the special block **53** is erased to serve as a back up block for further writing the unchanged data below the changed data in the block **52** page by  
15 page in a orderly manner according to the page number until completely filling the page, and then the original block **52** is erased to serve as a back up block, and the block **52** is replaced by the buffer **51** to repeat the above procedure.

**[0032]** The method of writing data to the flash memory cell of the present invention can continue writing data even when the data change is unable completely fill  
20 the page. Accordingly, the data writing speed can be increased substantially.

**[0033]** While the invention has been described in conjunction with a specific best mode, it is to be understood that many alternatives, modifications, and variations will be apparent to those skilled in the art in light of the foregoing description. Accordingly, it is

intended to embrace all such alternatives, modifications, and variations in which fall within the spirit and scope of the included claims. All matters set forth herein or shown in the accompanying drawings are to be interpreted in an illustrative and non-limiting sense.